

1 →

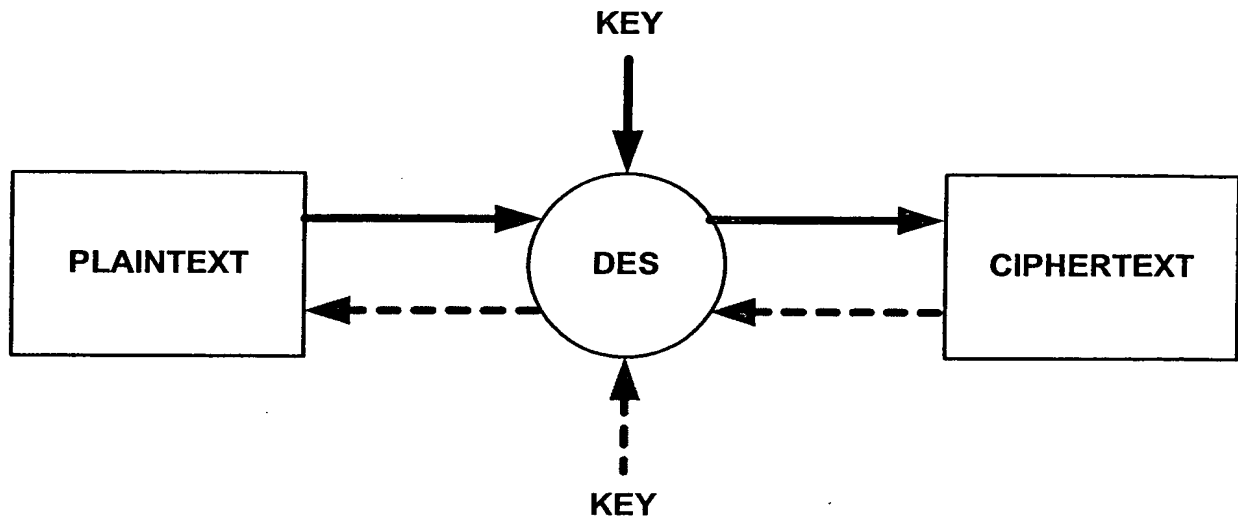
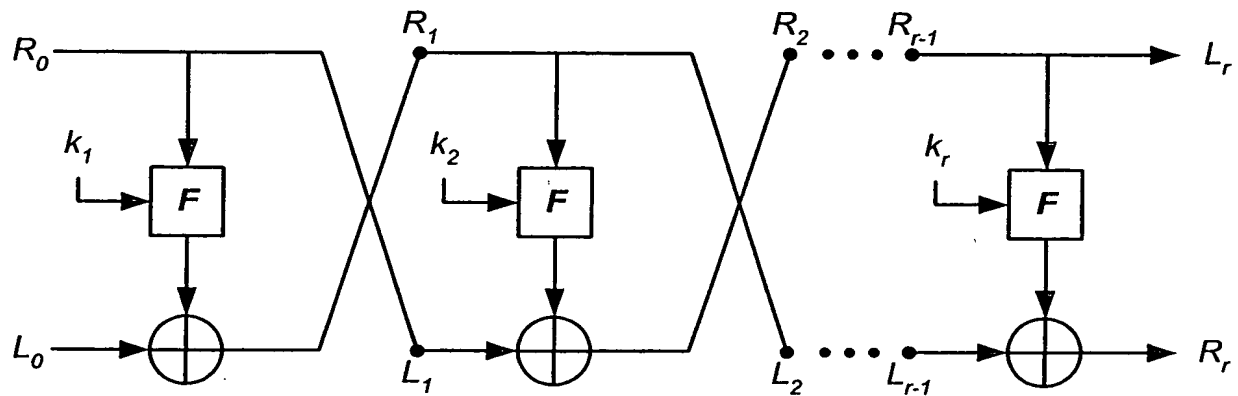


Fig. 1A

2 →



Feistel Cipher

Fig. 1B

2/15

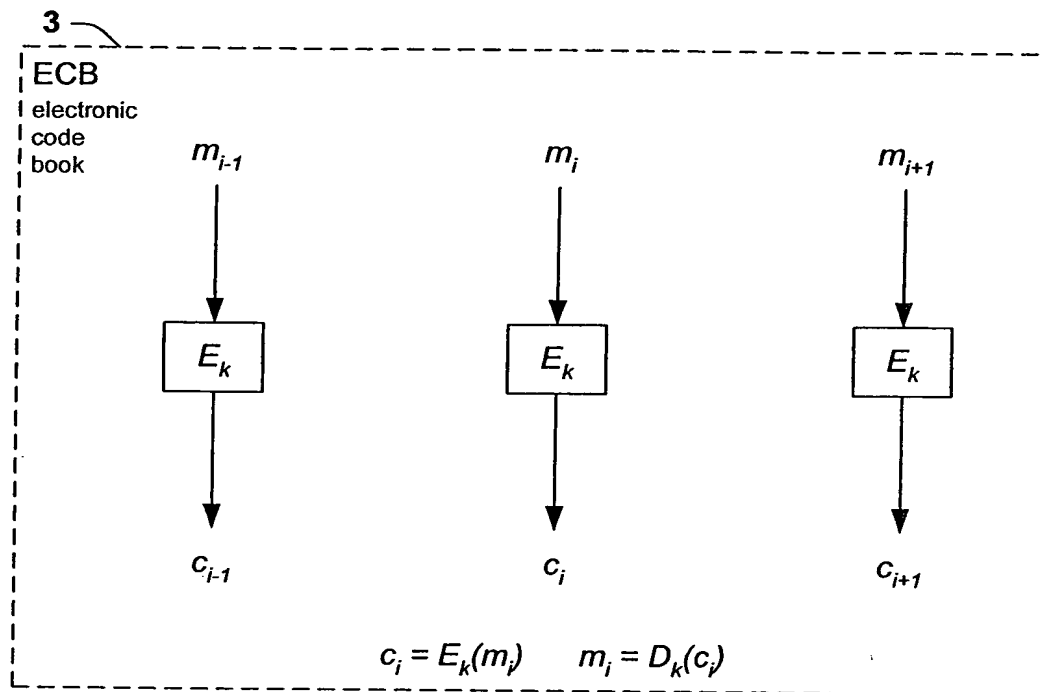


Fig. 1C

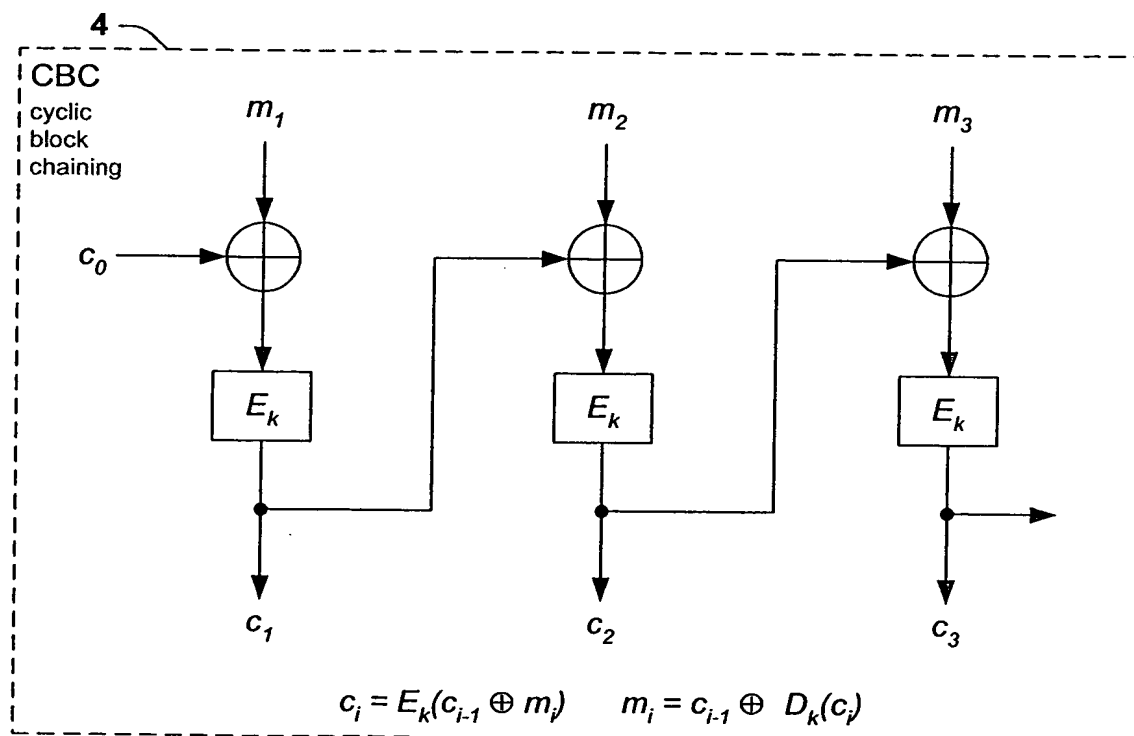


Fig. 1D

3/15

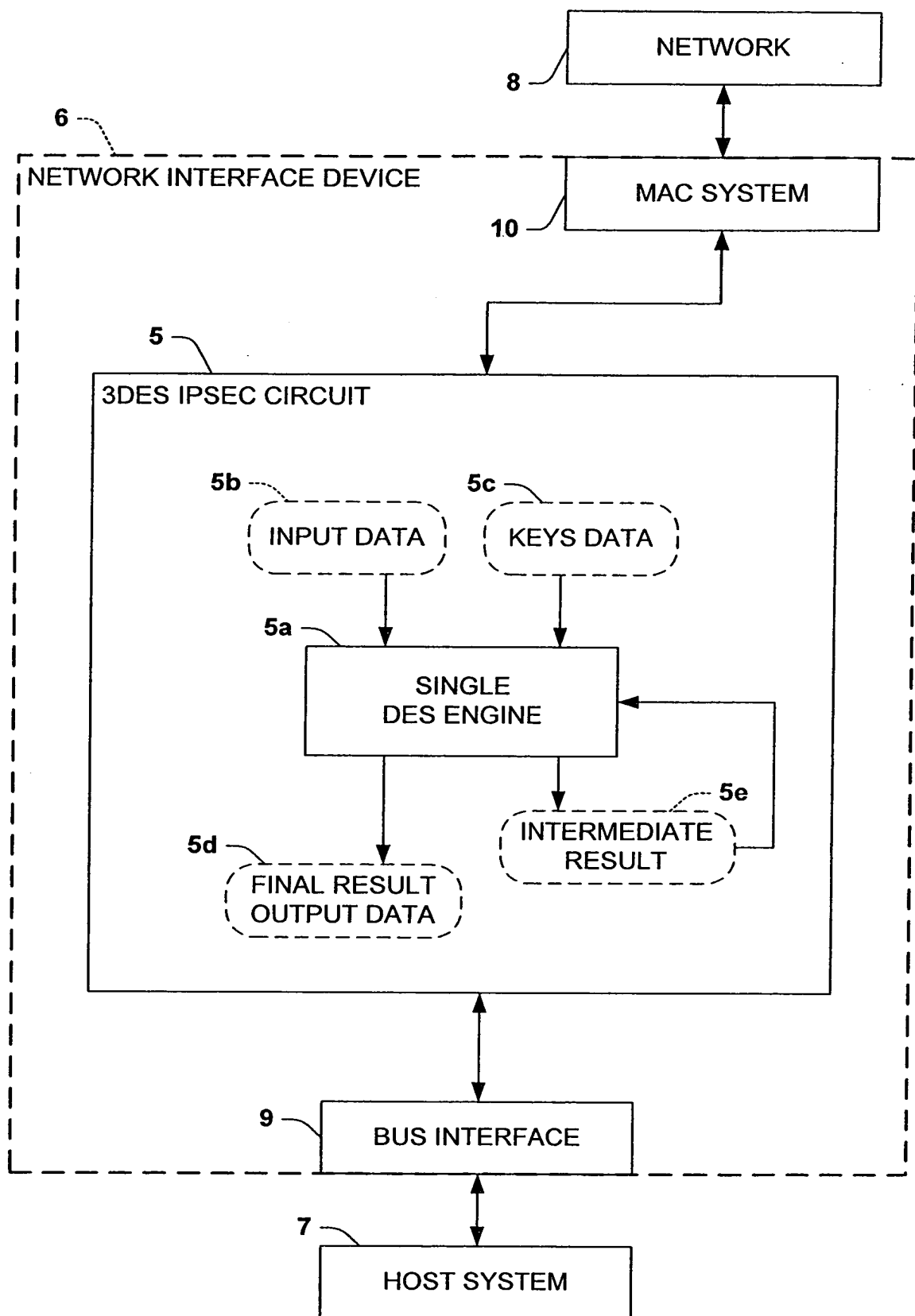


Fig. 1E

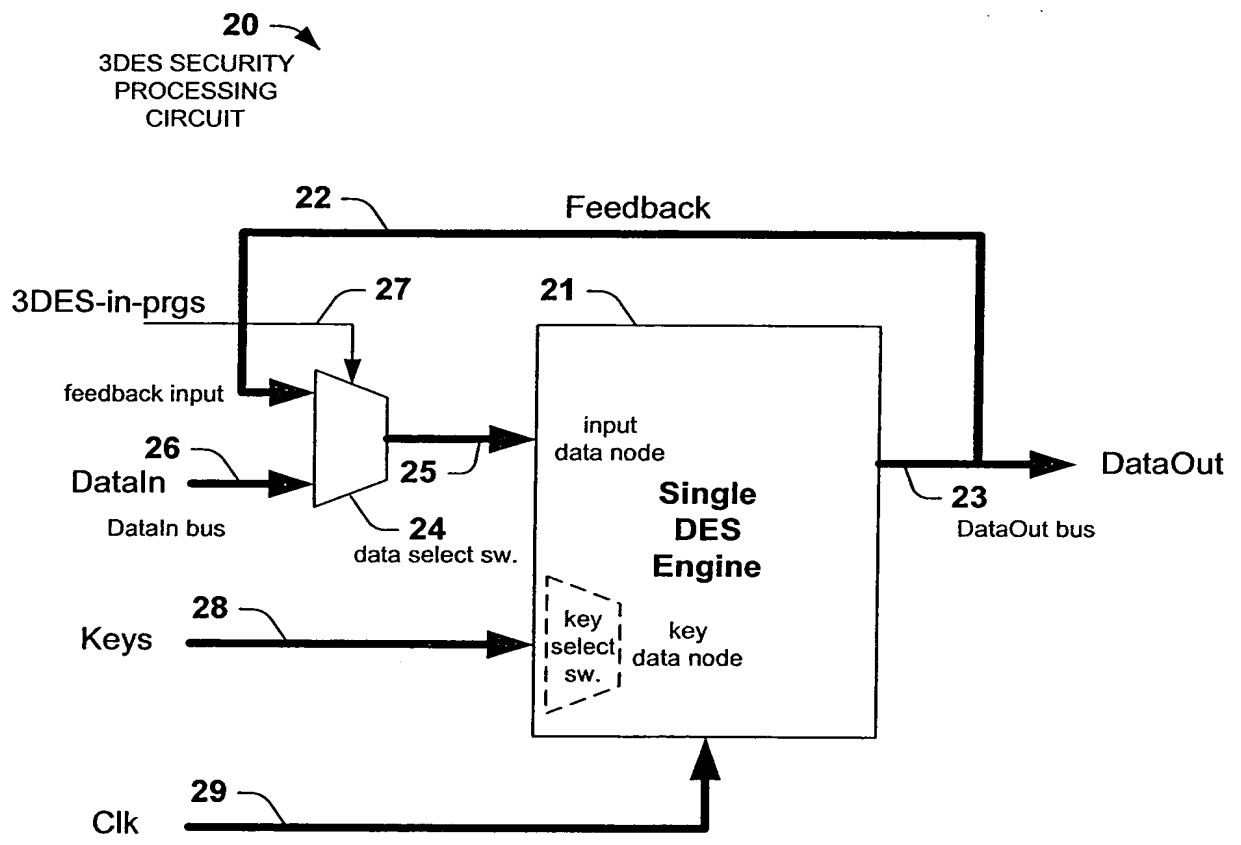


Fig. 1F

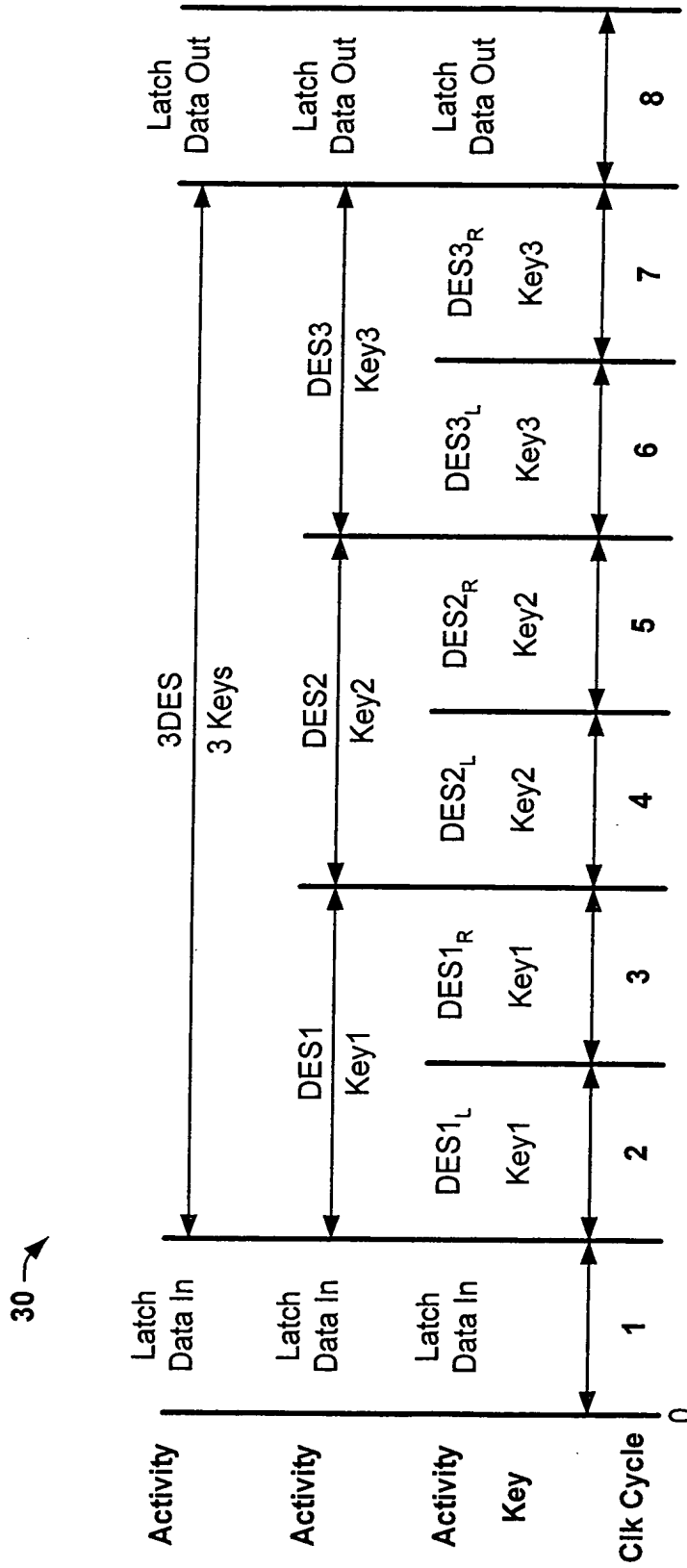


Fig. 1G



6/15

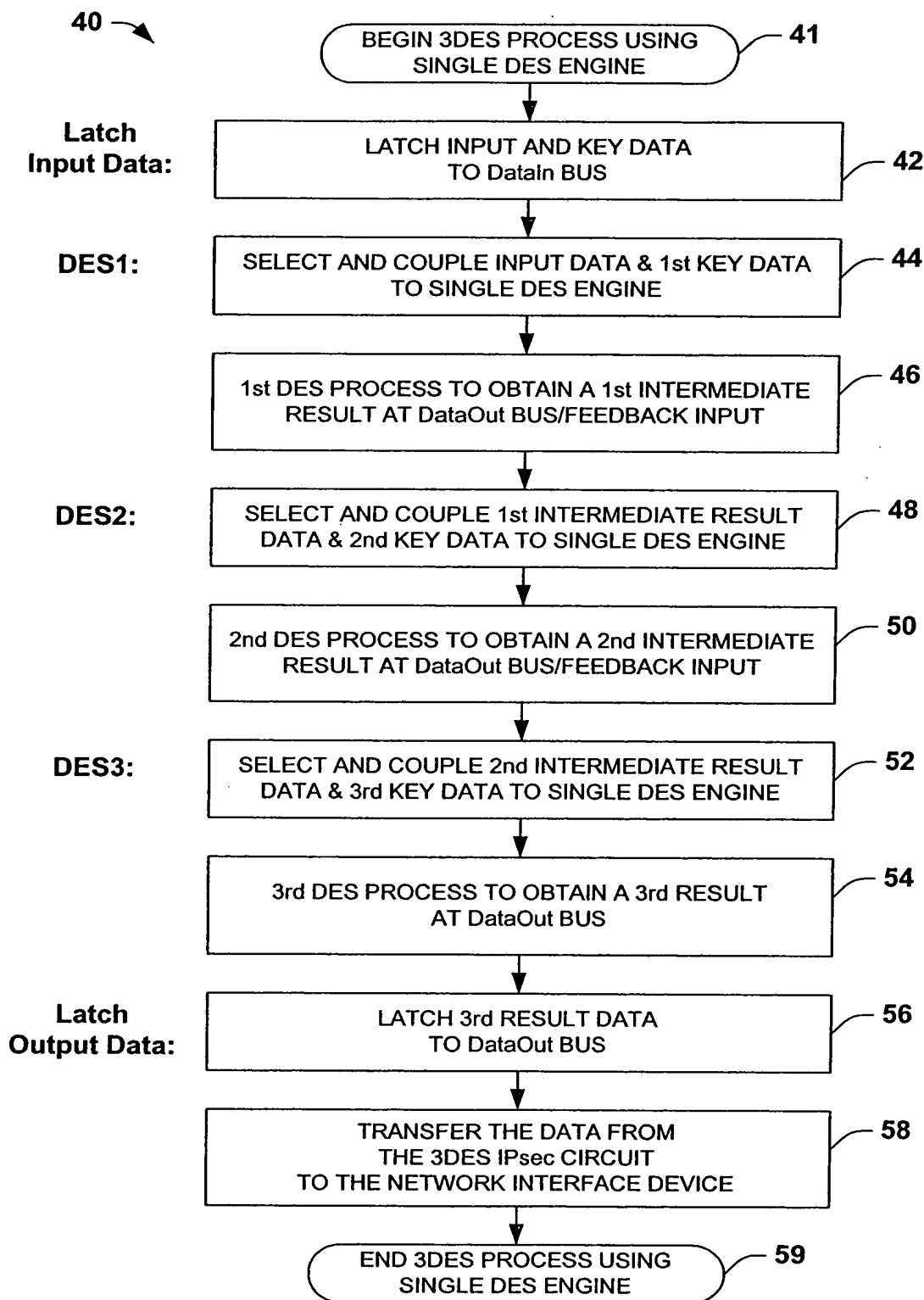


Fig. 1H

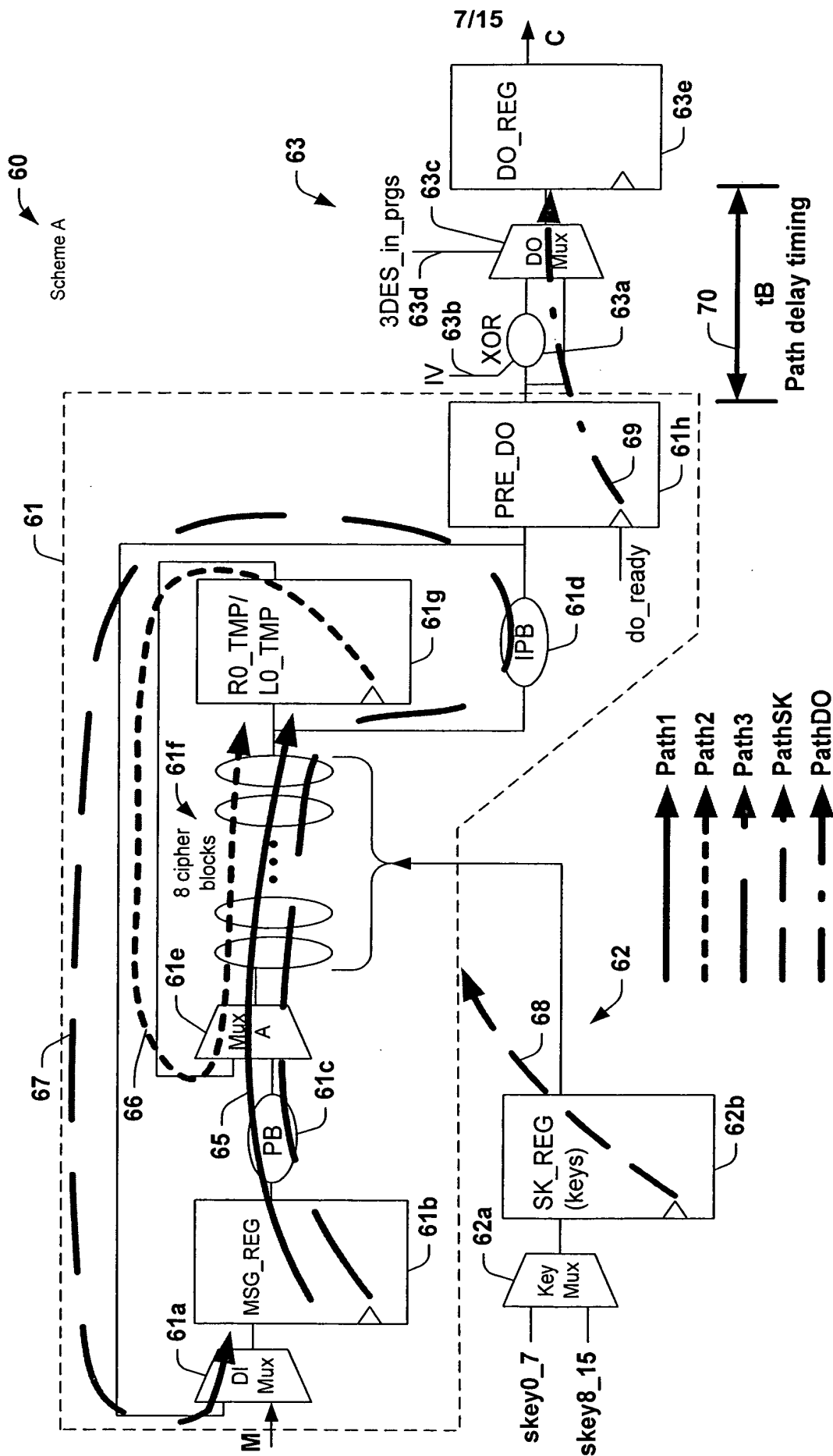


Fig. 11

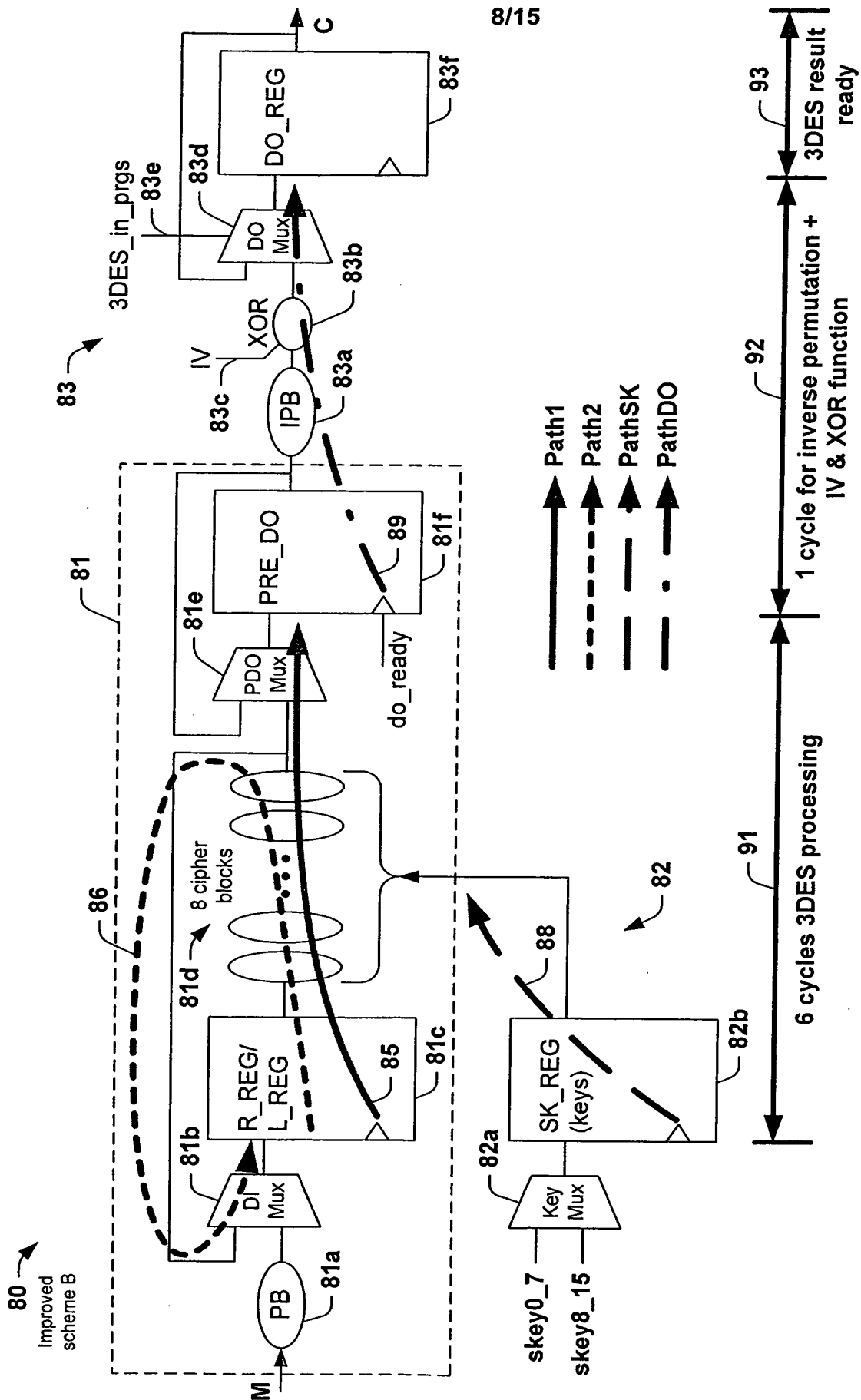


Fig. 1J

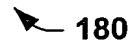




99

<b>Scheme A</b>	<b>Improved Scheme B</b>
<b>Path1 (1st 8 steps)</b>	<b>Path1 (1st 8 steps)</b>
PB	8 Cipher Blks
MuxA	PDO Mux
8 Cipher Blks	
<b>Path2 (2nd 8 steps)</b>	<b>Path2 (2nd 8 steps)</b>
MuxA	8 Cipher Blks
8 Cipher Blks	DI Mux
<b>Path3 (loopback for 3DES)</b>	<b>Path3 (equivalent- 1 or 2)</b>
PB	8 Cipher Blks
MuxA	DI Mux or PDO Mux
8 Cipher Blks	
IPB	
DI Mux	
<b>PathDO</b>	<b>PathDO</b>
IV XOR	IPB
DO Mux	IV XOR
	DO Mux
<b>PathSK</b>	<b>PathSK</b>
8 Cipher Blks	8 Cipher Blks
	PDO Mux

Fig. 1K



**Fig. 2**

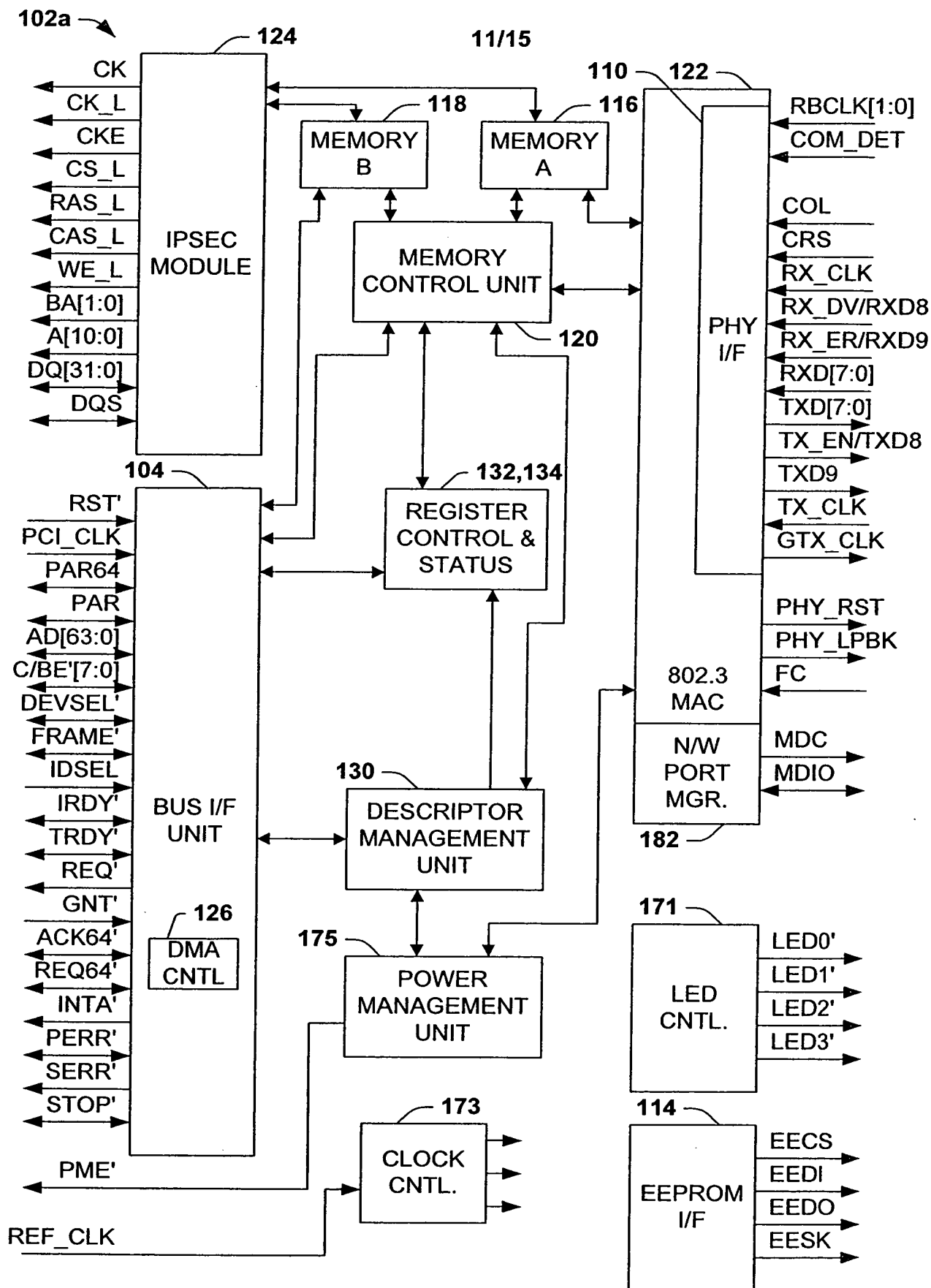


Fig. 3

12/15

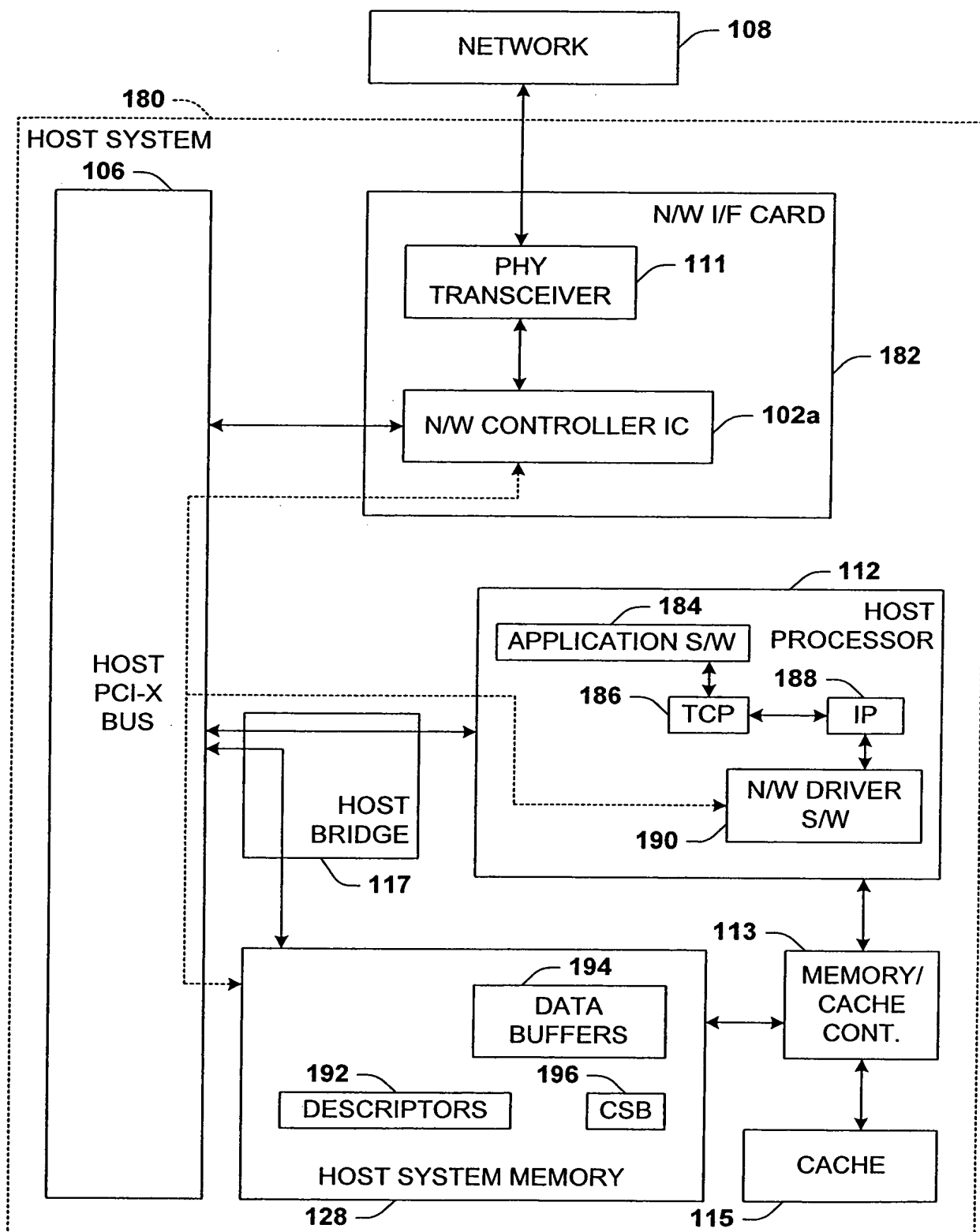
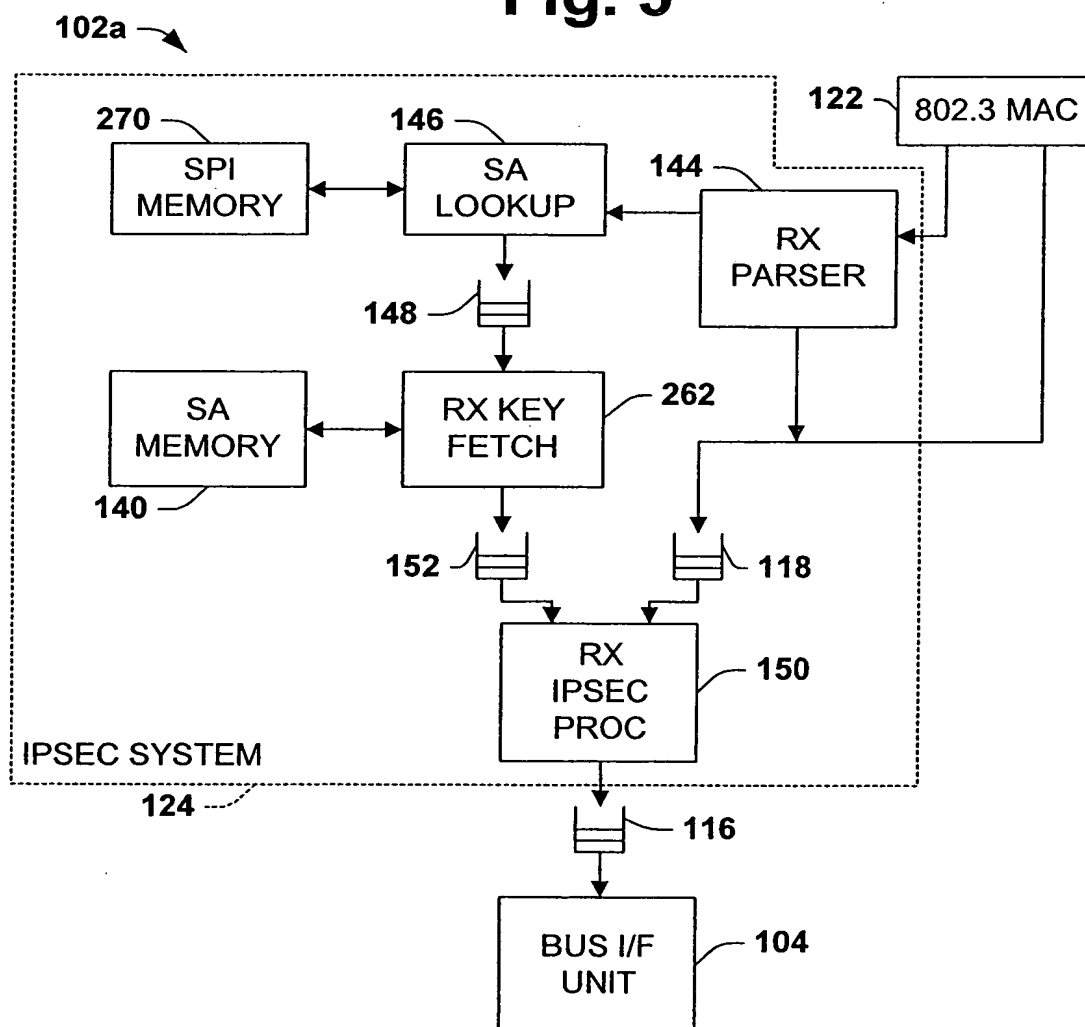
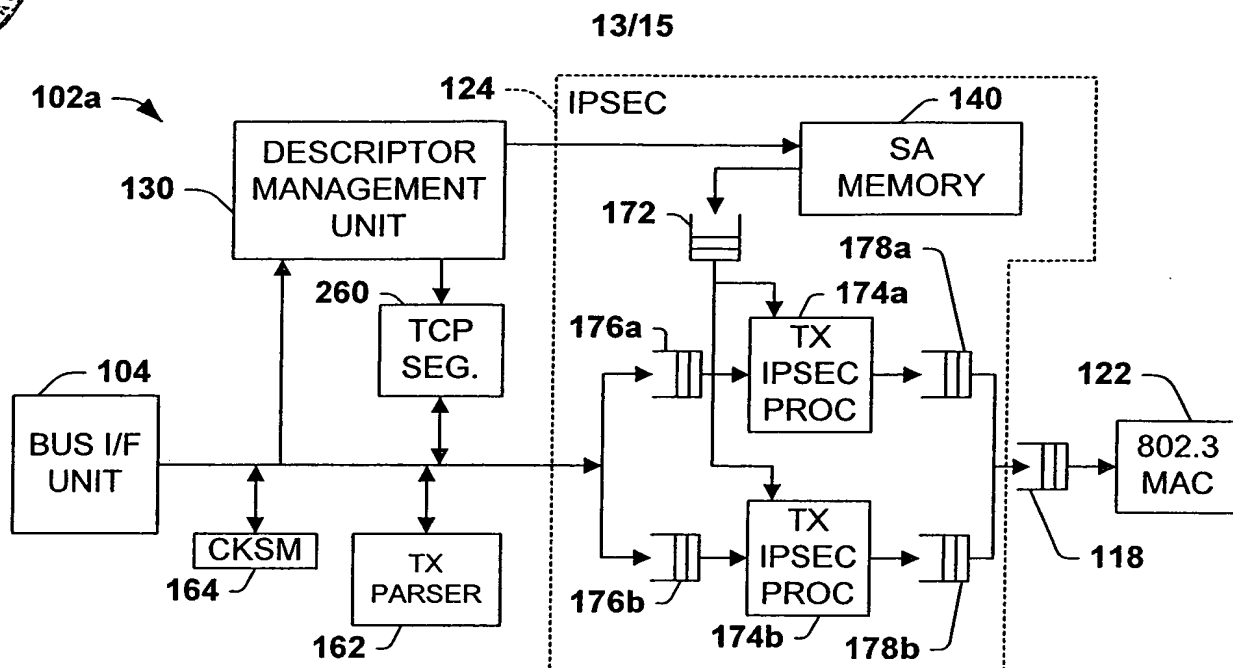


Fig. 4







15/15

140

SA MEMORY ENTRY

274

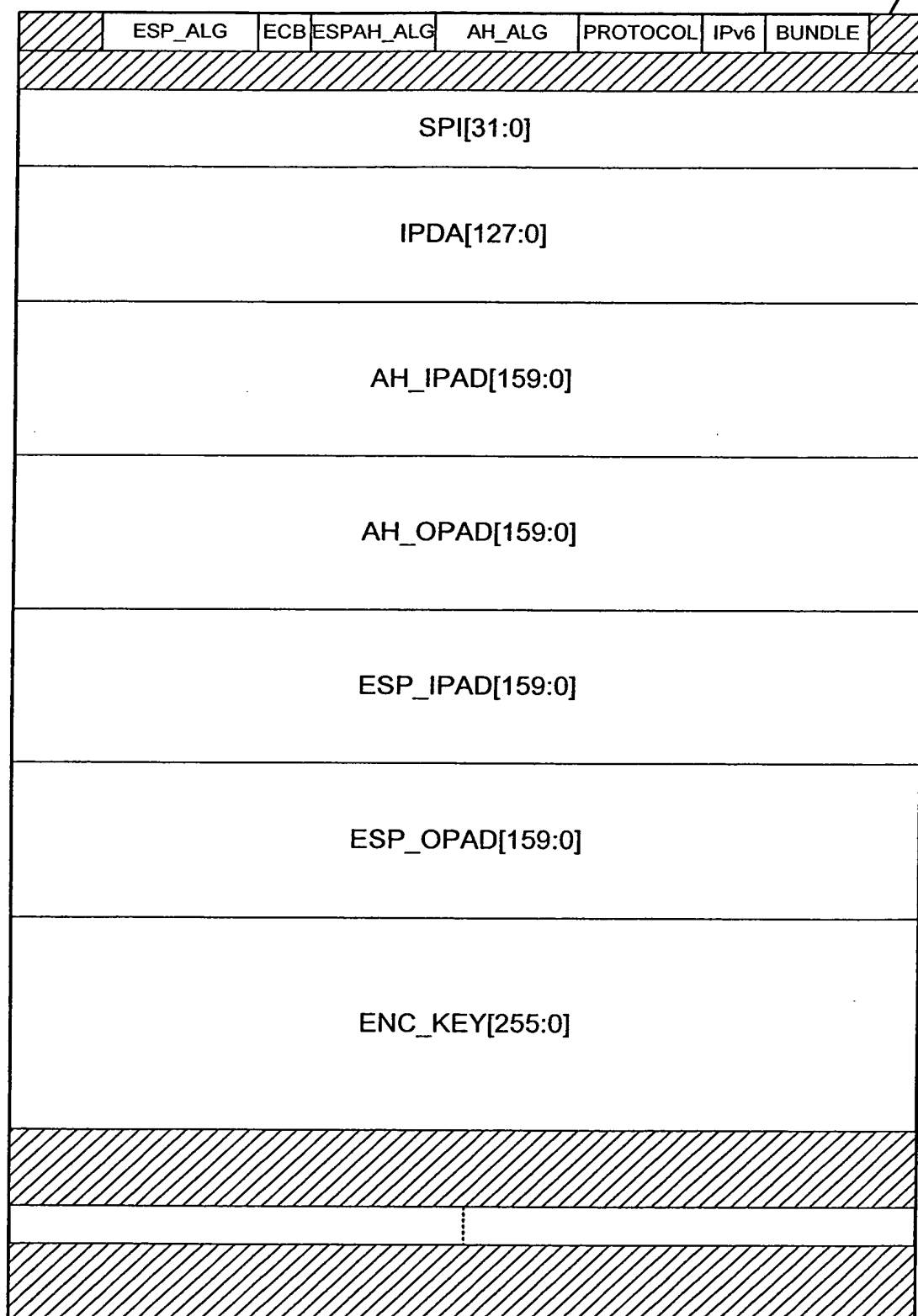


Fig. 7D